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## **Digital Down Converter Demo/Framework for HERON Modules with FPGA**

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The 'General Examples' provided by HUNT ENGINEERING include a Digital Down Conversion example. This example can be adapted to work on many of the FPGA modules, but in particular it has been provided as worked examples for the HERON-IO2v2 and the HERON-IO5v1.

The worked example for the HERON-IO2v2 is located under the CD directory 'fpga\io2v2\ddc\_example'. An example project is supplied for use with ISE along with ready to load bit-streams for the IO2v2.

For the HERON-IO5v1, the worked example is located under the directory 'fpga\io5v1\ddc\_example'. An example project is supplied for use with ISE along with ready to load bit-streams for the IO5v1.